| **Staff Name** | Dr.R.Avudaiammal |
| --- | --- |
| **Faculty ID** | TEC27 |
| **Designation** | Professor |
| **Qualification** | B.E, M.E, Ph.D |
| **Teaching Experience** | 29 Years 4 months |
| **Area of Specialization** | Multimedia Networks, Embedded Network Processor |
| **Subjects Handled** | 1. Signals and Systems, 2. Digital Signal Processing 3. Microprocessor and Microcontroller 4. Advanced Microprocessor 5. Linear Integrated Circuits 6. VLSI Design 7. Advanced Digital Signal Processing 8. Multi core Architecture |
| **Books Published** | 1. Reeba Korah, **R.Avudaiammal** Microprocessor and its Interfacing Devices – Dhanpat rai &Co -2002 2. Reeba Korah, **R.Avudaiammal** - Advanced Microprocessor and its Interfacing Devices Dhanpat rai &Co -2003 3. **R.Avudaiammal** , Information Coding Techniques , Vijay Nichole Pvt Limited 2005. |
| **Journals Published** | 1. **R.Avudaiammal,** A. Swarnalatha, “Implementation of Scalable Packet Classification Algorithm using Embedded Network Processor” International Journal of Applied Engineering Research, Vol.10, No.66, pp. 66-68, 2015.**.** 2. **Avudaiammal, R.** and Seethalakshmi, P.,“Design and Implementation of a Hybrid Packet Scheduling Algorithm on Network Processor Based Router for Enhancing QoS of Multimedia Applications”, European Journal of Scientific Research,Vol.72, No.2, pp. 245-262,2012. 3. **Avudaiammal, R.** and Seethalakshmi, P. “Efficient Mapping of Heuristic Packet Classifier on Network Processor based Router to Enhance QoS for Multimedia Applications”, Journal of Computing, Vol. 2, No. 9, pp. 107-113, 2010. 4. **Avudaiammal, R.** and Seethalakshmi, P., “Packet Scheduling Disciplines- A Survey”, International Journal of Engineering Research and Industrial Applications, Vol. 3, No. 1, pp. 375-399, 2010 5. **Avudaiammal, R.** and Seethalakshmi, P., “Bandwidth-Adaptive Scheduling for QoS Enhancement of Real-Time Multimedia Applications in Network Processor based Router”, Journal of Computer Science, Vol. 5, No. 12, pp. 1068-1074,2009. 6. **Avudaiammal, R.,** Sivasubramanian R., Pandian, R. and Seethalakshmi, P. , “TTSS Packet Classification Algorithm to Enhance Multimedia Applications in Network Processor based Router”, International Journal of Computer Science InformationSecurity, Vol. 2, No. 1, pp. 140-145, 2009. |
| **Conference / Workshop**  **Attended** | **International Conference**   1. **Avudaiammal, R.** and Seethalakshmi, P. Optimized Packet Classification Algorithm using Network Processor for Multimedia Applications,International conference on Advances in Recent Technologies in Communication and Computing, ARTCom,13 Sep 19-20, Bangalore 2. N.Aarthi, **R. Avudaiammal**, A.Swarnalatha, “ Multituple packet classification using Dynamic discrete bit selection Algorithm”, Proceedings of the 4th International Conference on Contemporary Engineering & Technology, ICCET’15 18th March 2015, at Sri Ramanujar Engineering college, Chennai. Chennai, India. ISBN-978-81-904760-6-5. 3. B. Arun Kumar, **R. Avudaiammal**, A.Swarnalatha“ QoS – based packet scheduler for hybrid wireless networks”, 1st International Conference on Electrical Computing Engineering & Communication, ITCECC’15 April 2015 , organized by International Institute of Scientific Research and Technology (IISRT), Chennai, India on 15th April 2015 4. Nixon Benetto, A.Swarnalatha, **R. Avudaiammal**, “ Detection of collaborative attacks in MANETS using Enhanced CBDS technique”. The International Conference on Science Technology Engineering and Management ICONSTEM’15, Jeppiar engineering College, Jeppiaar Nagar, Chennai. 5. R.**Avudaiammal** and Dr.P.Seethalakshmi, Implementation of Packet Classification algorithm in Network Processor based router to support Multimedia Applications, IMSAA .December’08. Bangalore. 6. **R.Avudaiammal** and Dr.P.Seethalakshmi A Novel scheduling algorithm in Network Processor based router to enhance Multimedia Services, pp46-50 ICSCI .January’09.Hyderabad   **National Conference**   1. **R.Avudaiammal** and Dr.P.Seethalakshmi An NP based packet scheduler to support multimedia packets, Recent trends in Soft computing,pp202-209 NCRTSC May’08 Bangalore. 2. **R.Avudaiammal** and D.Sujatha Implementation and performance analysis of Packet Classifier using NP for multimedia applications, Recent trends in Electronics, pp134-142 TRIGGER March’08. Chennai. 3. **R.Avudaiammal** and V.Christina Ruth NP based efficient packet scheduling algorithm for multimedia service,pp-43-47 NCCT’08, April’08 Chennai. 4. **R.Avudaiammal** and T.Annasudha , Hybrid Packet Scheduling Algorithm for QoS Enhancement of Real Time Multimedia Application on NP based Router pp 68-73 NCRTIC March’09. Chennai. 5. **R.Avudaiammal** and P.S.Cyrilla Destemona, Efficient Construction of HBTSS Packet Classification Algorithm to enhance Multimedia Application using NP pp 35-40 NCRTIC March’09. Chennai. 6. **R.Avudaiammal,** Shiju C Chacko and Dr.P.Seethalakshmi, NP Based Time Efficient Packet classification Algorithm To Enhance Multimedia Applications , pp 12, EMIT’10,Chennai. 7. **R.Avudaiammal** and J. Rini Devasudha, Design of Packet Classifier using Network Processor for Multimedia Applications, 2011 |
| **Patent Details** | - |
| **Funded Project Details** | - |